

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **: Kazuhisa KADA, et al.**
Filed: **: Concurrently herewith**
For: **: PARALLEL SIGNAL TRANSMISSION.....**
Serial No. **: Concurrently herewith**

December 27, 2001

Assistant Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

S I R:

Prior to the issuance of an Office Action, please amend the specification and claims as follows:

IN THE CLAIMS:

Please amend the following claims:

4.(Amended) The parallel signal transmission device as claimed in claim 1 wherein a parallel number in the parallel signals comprises a prime number.

5.(Amended) The parallel signal transmission device as claimed in claim 1 wherein a parallel number in the parallel signals and a bit number at a sampling interval latched by the latch are mutually in a prime relationship in a prime relationship.

6.(Amended) The parallel signal transmission device as claimed in claim 1 wherein the bit shift detector has a detecting unit for detecting the bit shift amounts of the parallel signals from the

synchronizing signals by performing a code matching between the serial code signals paralleled and parallel signals outputted from the bit changeover portion.

8.(Amended) The parallel signal transmission device as claimed in claim 1 wherein the bit shift detector detects the bit shift amounts of the parallel signals in a predetermined stage of forward and backward protection units.

11.(Amended) The parallel signal transmission device as claimed in claim 9 wherein the bit shift detector has a clearing unit for clearing the protection units of the corresponding parallel signals when the synchronization established state is not obtained.

12.(Amended) The parallel signal transmission device as claimed in claim 1 wherein the bit shift detector has a warning unit for generating a warning when the synchronization established state is not obtained for a predetermined time.

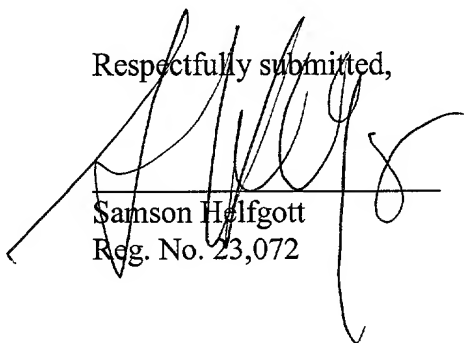
REMARKS

This amendment is being submitted to avoid multiple dependent claims.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned **"Version with markings to show changes made."**

Favorable consideration is respectfully requested.

Respectfully submitted,



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Docket No.: FUJZ 19.303
Any fee due with this paper, may be charged
On Deposit Acct. No. 50-1290.

VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS:

Please amend the following claims:

4.(Amended) The parallel signal transmission device as claimed in ~~any one of~~ claims 1 ~~to 3~~ wherein a parallel number in the parallel signals comprises a prime number.

5.(Amended) The parallel signal transmission device as claimed in ~~any one of~~ claims 1 ~~to 4~~ wherein a parallel number in the parallel signals and a bit number at a sampling interval latched by the latch are mutually in a prime relationship in a prime relationship.

6.(Amended) The parallel signal transmission device as claimed in ~~any one of~~ claims 1 ~~to 5~~ wherein the bit shift detector has a detecting unit for detecting the bit shift amounts of the parallel signals from the synchronizing signals by performing a code matching between the serial code signals paralleled and parallel signals outputted from the bit changeover portion.

8.(Amended) The parallel signal transmission device as claimed in ~~any one of~~ claims 1 ~~to 7~~ wherein the bit shift detector detects the bit shift amounts of the parallel signals in a predetermined stage of forward and backward protection units.

11.(Amended) The parallel signal transmission device as claimed in claim 9 ~~or 10~~ wherein the bit shift detector has a clearing unit for clearing the protection units of the corresponding parallel signals when the synchronization established state is not obtained.

12.(Amended) The parallel signal transmission device as claimed in ~~any one of claims 1 to 11~~
wherein the bit shift detector has a warning unit for generating a warning when the
synchronization established state is not obtained for a predetermined time.

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